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Atty. Docket No.: 042390.P1674DC	Application No.: 08/636,024
Applicant: Pippin, J.	
Filing Date: April 19, 1996	

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Form PTO-1449 (Modified)

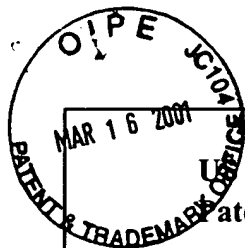
## US Patent Documents

Technology Center 2100

Examiner's Initials	Date	Document Number	Name	Class	Sub- Class	Filing Date
AB	3/23/93	5,195,827	Audy et al.	374	172	
	12/17/96	5,586,270	Rotier et al.	395	282	
	2/23/93	5,189,314	Georgiou et al.	307	271	
	10/25/94	5,359,234	Atriss et al.	307	310	
	1/9/96	5,483,102	Neal et al.	257	712	
↓	11/17/98	5,838,578	Pippin, Jack D.	364	488	
AB	2/21/89	4,807,144	Joehlin et al.	364	473	
		H562	Trachier et al.			
		Re. 32,960	Levine			
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		4,779,161	DeShazo, Jr.	361	106	
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		4,924,112	Anderson et al.	326	32	
		5,008,771	Palara	361	103	
		5,105,366	Beckey	364	505	
		5,453,682	Hinrichs et al.	324	132	
		4,799,176	Cacciatore	364	557	
		5,287,292	Kenney et al.	364	550	
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		4,903,106	Fukunaga et al.	357	43	
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		5,149,199	Kinoshita et al.	374	175	
		5,170,344	Berton et al.	364	400	
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		5,325,256	Weng et al.	364	557	
		5,359,236	Giordano et al.	307	310	

Examiner <i>Samuel Broda</i>	Date Considered <i>20 May 2003</i>
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<b>U.S. Department of Commerce</b> <b>Patent and Trademark Office</b>  <b>Form PTO-1449 (Modified)</b>	<b>Atty. Docket No.:</b> 042390.P1674DC	<b>Application No.:</b> 08/136,024/09/707,860
	<b>Applicant:</b> Pippin, J.	
	<b>Filing Date:</b> November 7, 2000	

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**US Patent Documents**

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Examiner's Initials	Date	Document Number	Name	Class	Sub- Class	Filing Date
AB	2/6/96	5,490,059	Mahalingaiah, et al.	364	166	
↓	3/3/98	5,723,998	Saito, et al.	327	513	
↓	1/16/96	5,485,127	Bertoluzzi et al.	331	69	
↓	1/9/90	4,893,097	Zwack	331	176	
↓	8/25/98	5,798,667	Herbert	327	513	

**Foreign Patent Documents**

Examiner's Initials	Date	Document Number	Country	Class	Sub- Class	Translation
AB	5/16/95	0683558A1	EPO			

**Other Documents (Include Author, Title, Date, Pages)**

AB	Analog Devices, Low Power, Programmable Temperature Controller", pps. 1-16, 1995.
	Chin, Shu-Yuan and chung-Yu Wu, "A New Type of Curvature-Compensated CMOS Bandgap Voltage References, VLSITSA, 1991, pps. 398-402.
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AB	Sanchez et al., "Thermal Management System for High Performance PowerPC Microprocessors", pps. 325-330, 1997.

<b>Examiner</b> <i>Samuel Broda</i>	<b>Date Considered</b> <i>20 May 2003</i>
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